

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit having, formed on a single semiconductor chip, a receiving circuit unit for separating a multiplex signal from outside into signals on a plurality of channels, supplying them to outside, and generating a clock signal from the input signal, a loopback path for taking out, by branching, the signals separated by the receiving circuit unit, a selector for receiving input channel signals from the outside, receiving the separated channel signals from the receiving circuit unit via the loopback path, and selecting either the channel signals from the outside or the channel signals from the loopback path responsive to a test mode signal, and a transmitting circuit unit for multiplexing the output from the selector in a time sharing manner and supplying the multiplex signal to the outside.

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